

Docket No.: 50432-657

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
MARK T. RAMSBNEY, et al. : Confirmation Number:
Serial No.: Divisional of Appl. No.
09/143,089 : Group Art Unit:
Filed: November 25, 2003 : Examiner:
For: METHODS AND ARRANGEMENTS FOR REDUCING FALSE PROGRAMMING IN
NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES

TRANSMITTAL OF FORMAL DRAWINGS

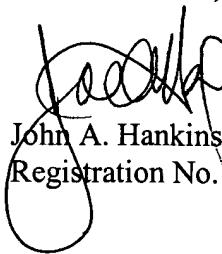
Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Five (5) sheets of formal drawings are submitted herewith.

Respectfully submitted,

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Date: November 25, 2003

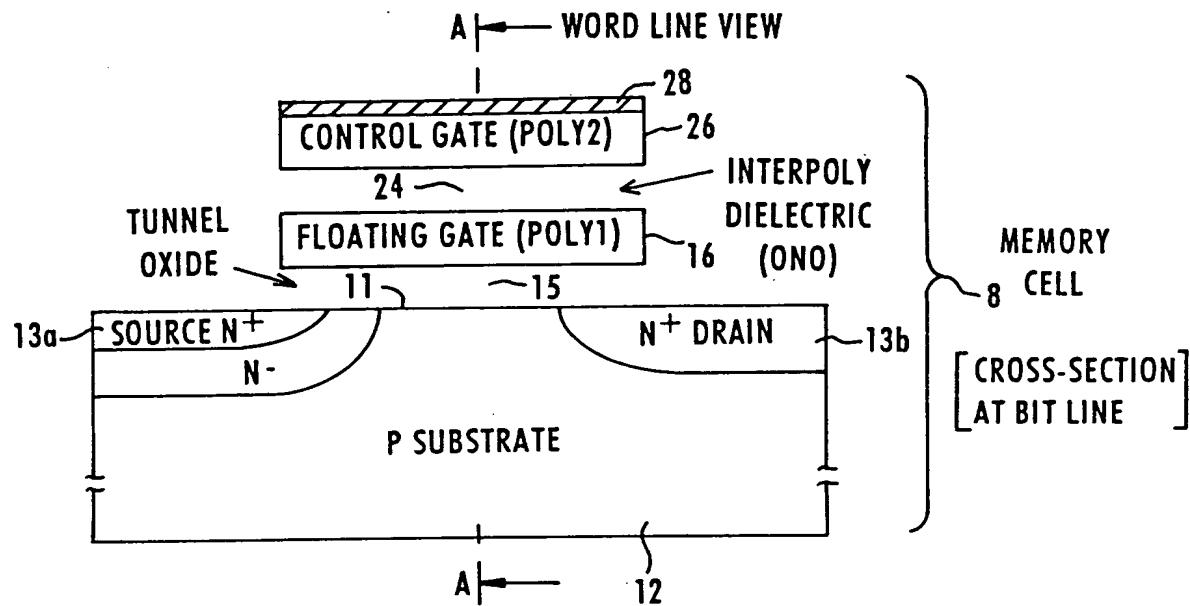


FIG. 1a (PRIOR ART)

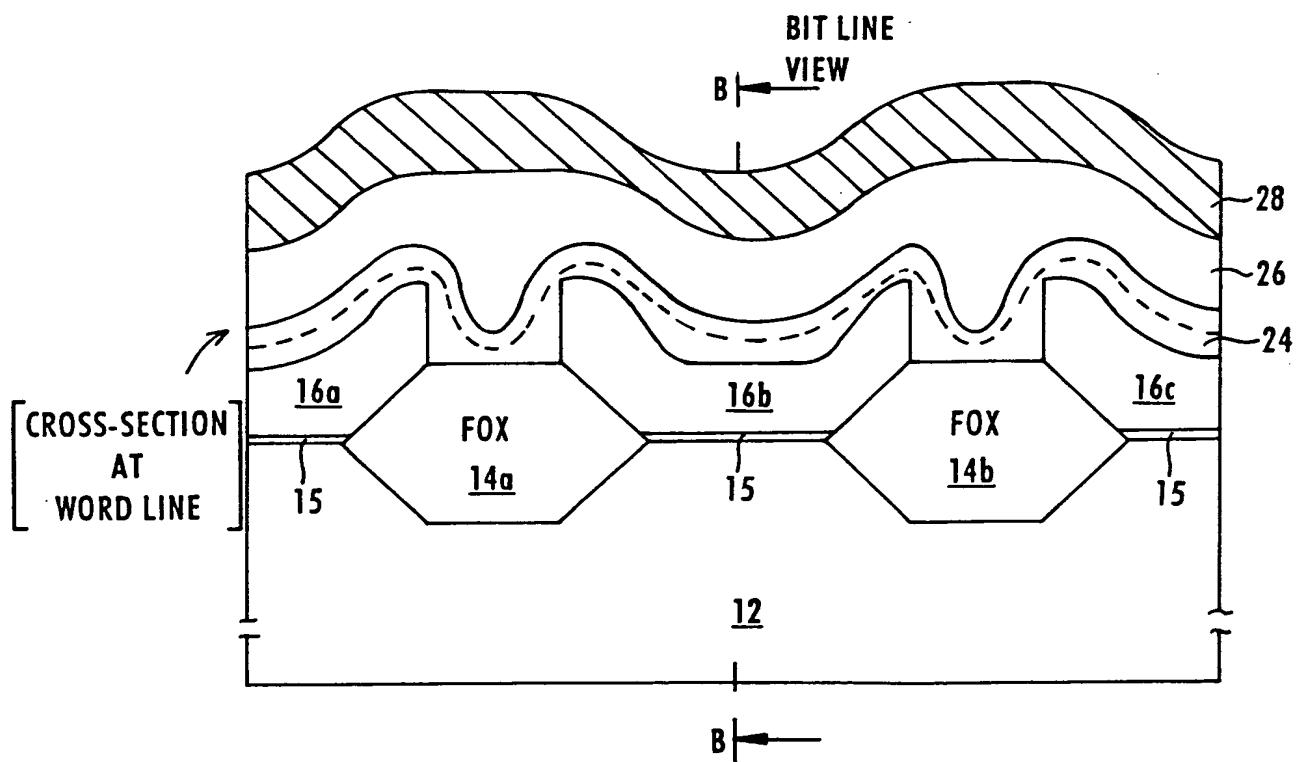


FIG. 1b (PRIOR ART)

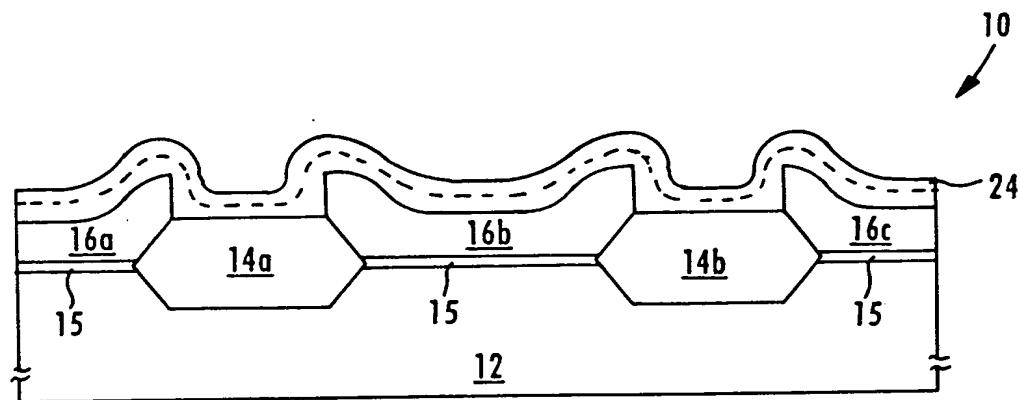


FIG. 2A (PRIOR ART)

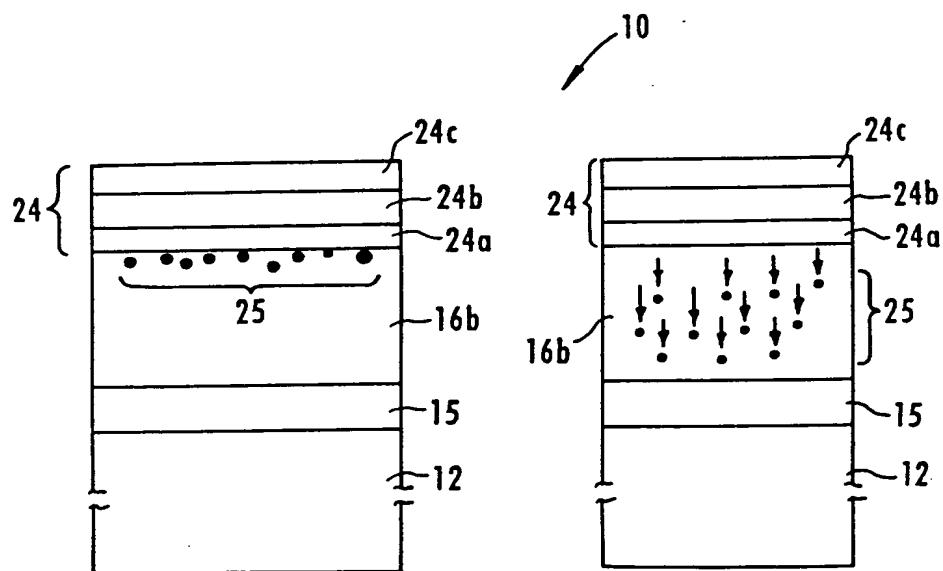
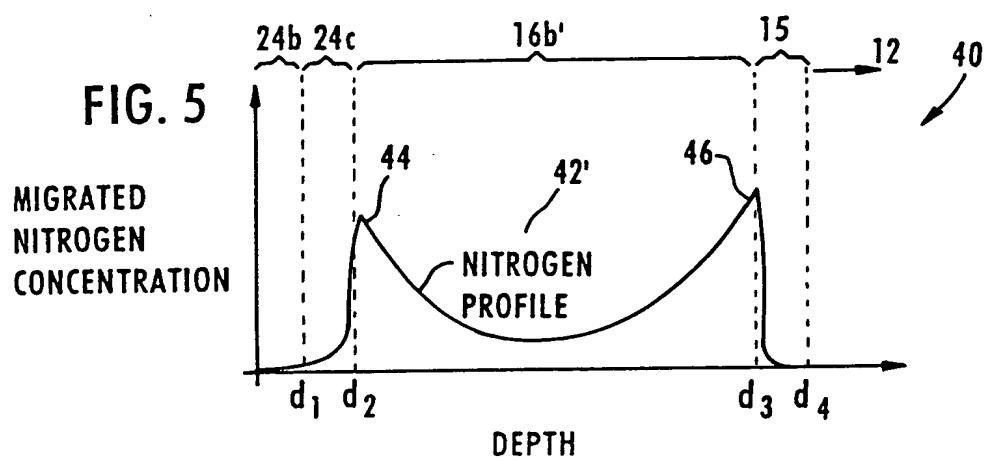
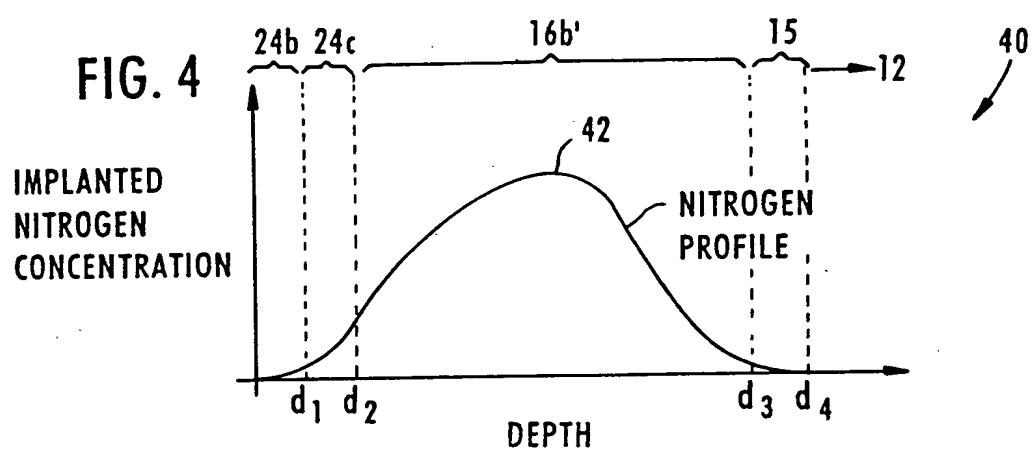
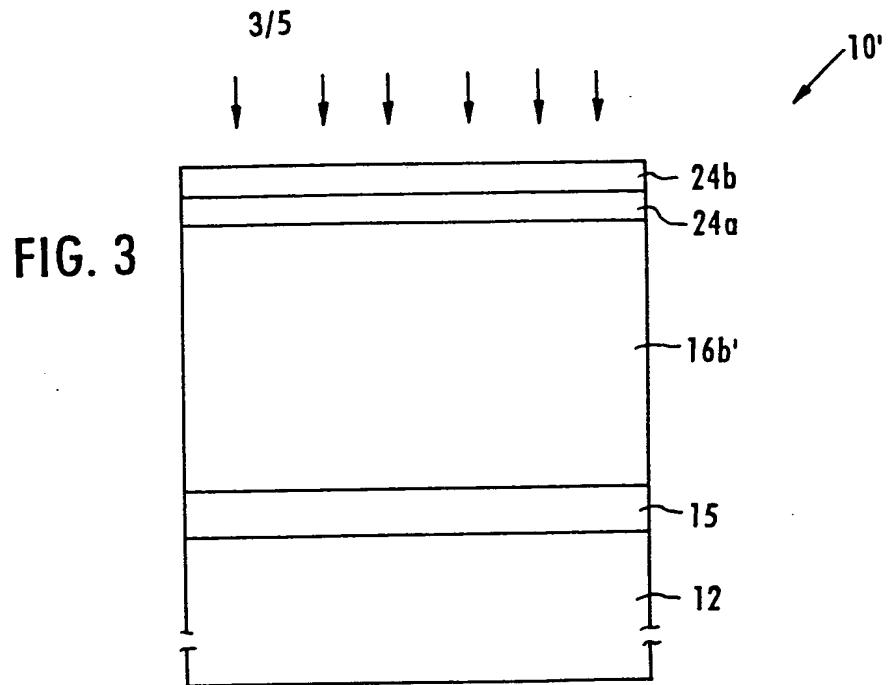


FIG. 2B
(PRIOR ART)

FIG. 2C
(PRIOR ART)



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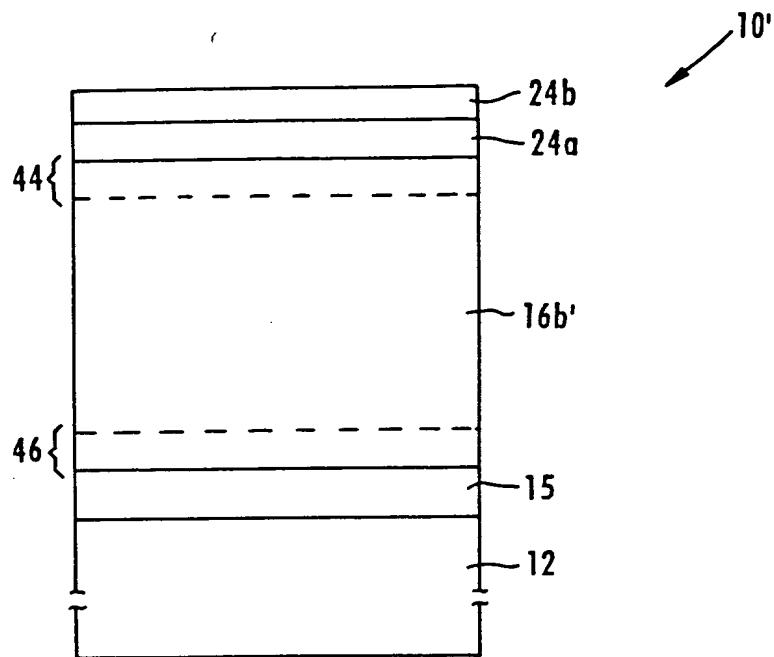


FIG. 6

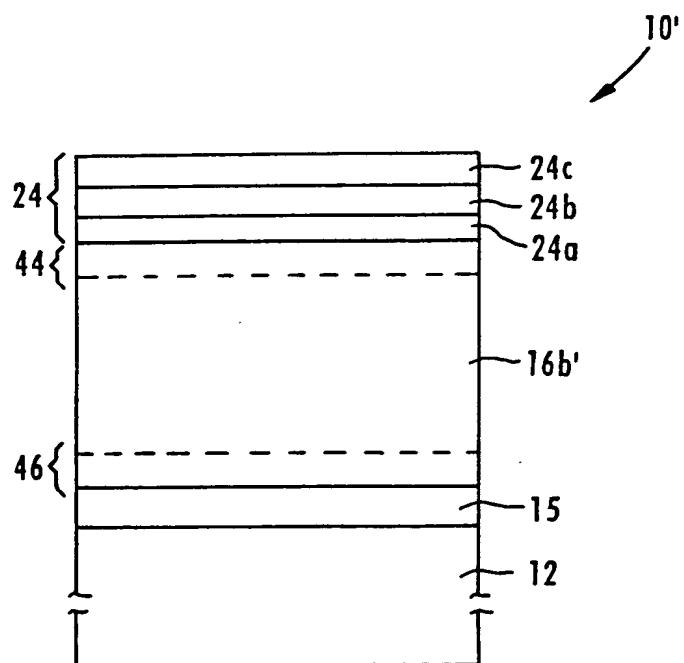


FIG. 7

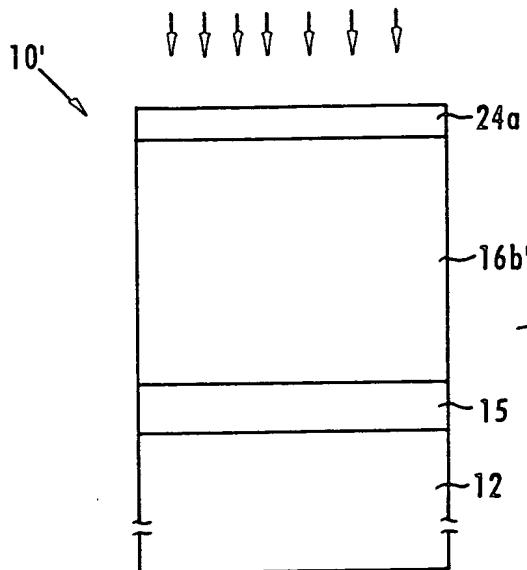


FIG. 8a

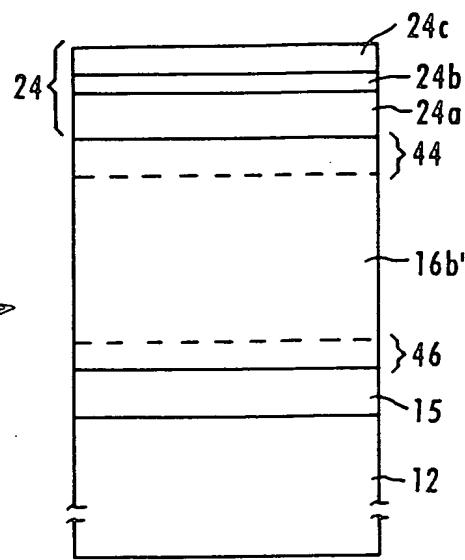


FIG. 8b

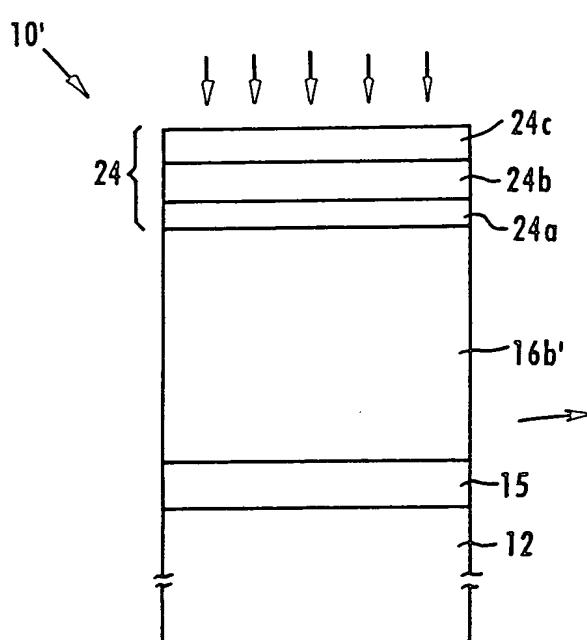


FIG. 9a

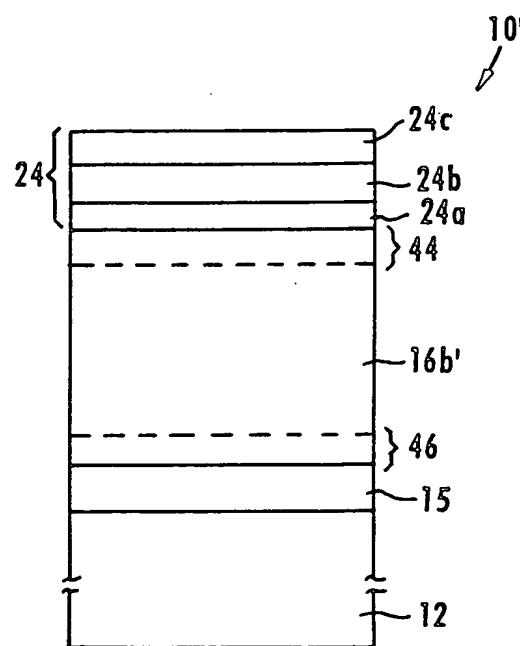


FIG. 9b